

	Type	L #	Hits	Search Text	Dbs	Time Stamp
1	IS&R	L1	1	("4608035").PN.	USPAT	2005/04/21 11:08
2	IS&R	L2	1	("4648035").PN.	USPAT	2005/04/21 11:08
3	BRS	L3	28	("4648035").URPN.	USPAT	2005/04/21 11:29
4	BRS	L4	6	("3426332" "4056819" "4315310" "4407016" "4433376").PN.	US-PGPUB; USPAT; USOCR	2005/04/21 11:09
5	BRS	L5	240	(cpu near2 core) and (one-chip or chipset or system-on-a-chip)	USPAT	2005/04/21 11:55
6	BRS	L6	21	((cpu near2 core) or lsi) and ((one-chip or chipset or system-on-a-chip) with (address near5 space))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 11:58
7	BRS	L7	8	((cpu near2 core) or lsi) with (address near5 space)) and (one-chip or chipset or system-on- a-chip)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 11:58
8	BRS	L8	27	6 or 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 11:58

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	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	9	(cpu with lsi) same (address near5 space)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:23
2	BRS	L2	817	(cpu with lsi) same (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:23
3	BRS	L3	669	cpu with lsi with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:24
4	BRS	L4	19	(cpu near2 core) with lsi with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:57
5	BRS	L5	6	(cpu near2 core) with ip with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:58

	Type	L #	Hits	Search Text	Dbs	Time Stamp
1	BRS	L1	9	(cpu with lsi) same (address near5 space)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:23
2	BRS	L2	817	(cpu with lsi) same (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:23
3	BRS	L3	669	cpu with lsi with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:24
4	BRS	L4	19	(cpu near2 core) with lsi with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:57
5	BRS	L5	6	(cpu near2 core) with ip with (integrated or embedded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 12:58
6	BRS	L6	5668	(volatile or non-volatile or nonvolatile or flash or eeprom or rom or eeprom) with ((translation or translate or translated or translating or map or mapping or mapped or convert or converting or converted or conversion) with (address or addressed or addressing))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 13:27
7	BRS	L7	3004	(volatile or non-volatile or nonvolatile or flash or eeprom or rom or eeprom) with ((translation or translate or translated or translating or map or mapping or mapped or convert or converting or converted or conversion) near3 (address or addressed or addressing))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 13:26
8	BRS	L8	485	6 and lsi	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 13:27
9	BRS	L9	95	6 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/04/21 14:15
10	BRS	L10	2	("6292851").URPN.	USPAT	2005/04/21 14:14
11	BRS	L11	3	("5652839" "5890204" "5996030").PN.	US-PGPUB; USPAT; USOCR	2005/04/21 14:15

12	IS&R	L12	1	("6292851").PN.	USPAT	2005/04/21 14:16
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	Type	L #	Hits	Search Text	DBs	Time Stamp
13	BRS	L14	1	12 and (flash or "30")	USPAT	2005/04/21 14:23
14	BRS	L15	1	12 and (line adj1 interface)	USPAT	2005/04/21 14:23